Kirk Bresniker

Chief Architect and HPE Fellow/VP
CTO and Hewlett Packard Labs
Hewlett Packard Enterprise

Kirk Bresniker is Chief Architect of Hewlett Packard Labs and a Hewlett Packard Enterprise Fellow and Vice President. He joined Labs in 2014 to drive The Machine Research and Advanced Development program, leading teams across Labs and across HPE business units with the goal of demonstrating and evangelizing the benefits of Memory-Driven Computing. His current focus is accelerating the transfer of technologies from The Machine research program in order to drive differentiating value into existing product categories as well as disruptive new offerings. Prior to joining Labs, Kirk was Vice President and Chief Technologist in the HP Servers Global Business Unit representing 25 years of innovation leadership.

Joining HP as a PA-RISC system hardware engineer in 1989, he has always been a part of the Business Critical Systems team. In 1993, he was the design lead for the first HP entry-level multi-processor system which broke new ground in terms of low-cost, high performance design. Following that, he was the design lead for the entire D-class PA-RISC product family, one of the highest volume BCS product families ever introduced.   Starting in 1997, Kirk began a decade-long research and development effort to develop novel new modular system architectures which would eventually become a new category of integrated hardware and software offerings known as Blade Servers.   This early work was eventually refined and blended with the contributions of the combined HP-Compaq merger lead to become HP BladeSystem c-Class, the undisputed leader in Blade Server platforms. In 1999, he was the key architect for all PA-RISC and Itanium entry and mid-range servers and oversaw a complete re-vamp of the product line and prepared for the PA-RISC to Itanium transition. From 2000 onwards, Kirk oversaw the transformation of the HP-UX UNIX and fault tolerant NonStop to blades to extend BladeSystem to the mission critical market, culminating in the Superdome X mission critical X86 blade platform, the highest performing HPE Mission Critical ProLiant system ever created. It was also during this period that he led the earliest investigations into what would become The Machine Research program.

Kirk current holds 28 US and 10 foreign patents in areas of modular platforms and blade systems, integrated circuits, and power and environmental control. He graduated in 1989 Cum Laude from Santa Clara University with a BSEE and was also a member of the Humanities Honors program.

1501 Page Mill Road
Building 1L
Palo Alto, CA 94304
USA

https://www.linkedin.com/in/kirkbresniker/

hpe.com